

- Drafts
- Pending
- Active
 - ⌚ L1: (3724) calculate\$4 near9 remaining near9 time
 - ⌚ L2: (32) buffer near9
 - ⌚ L3: (16) buffers\$4 with start\$4 with time with (speed rate) with full
 - ⌚ L4: (280) buffer\$4 with time with (speed rate) with full
 - ⌚ L5: (571) shin.xp.
 - ⌚ L6: (785925) buffer
 - ⌚ L7: (348) 5 and 6
 - ⌚ L8: (575) shin.xa.
 - ⌚ L9: (57) 6 and 8 and early
 - ⌚ L10: (234) 6 with full with empty with time
 - ⌚ L11: (705) 6 near9 transfer\$4 near9 free
 - ⌚ L12: (35) (start\$4 begin\$5) with 11
 - ⌚ L13: (0) calculate\$5 near9 (time space) near9 buffer
 - ⌚ L14: (2289) calculate\$5 near9 (time space) near9 buffer
 - ⌚ L15: (110) (remaining available) with 14
 - ⌚ L16: (1) ("5944802"),PN
 - ⌚ L17: (0) FULL AND 16
 - ⌚ L18: (1) EMPTY AND 16
 - ⌚ L19: (149) ("5683749") or ("5732268") or ("5982725") or ("6192484") or
- Failed
- Saved
- Favorites
- Tagged (8)

DR. JESSE BROWN

~~Exhibit 1000000~~ DR

〔五〕新編中華書局影印

U	Ref	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Class	Entered
1	Γ	US 200403064607 A1	20040401	14	Electronic apparatus and startup contr.	710/57			Odakura; Yasu
2	Γ	US 5944802 A	19990831	11	Dynamic speed matching of host chan	710/52	710/53		Bello; Keith A
3	Γ	US 6516359 B1	20030204	17	Information processing method and a	710/52	709/219		Kurihara; Na
4	Γ	US 5450546 A	19950912	43	Intelligent hardware for automatically	710/57	710/1		Kraklian; Cr
5	Γ	US 5884399 A	19990316	18	Control circuit for a buffer memory to t	710/52	710/57		Klingelhofer; J
6	Γ	US 5729708 A	19980317	82	Portable data buffer apparatus with mi	710/52			Satoh; Makoto
7	Γ	US 5121479 A	19920609	15	Early start mode data transfer apparat	710/34			O'Brien; John
8	Γ	US 5941962 A	19990824	12	Buffer management system for releasing	710/53	710/56		Hirano; Keizo

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Pending

Active

L1: (3724) calculate\$4 near? remaining near? time

L2: (32) buffer near? 1

L3: (16) buffer\$4 with start\$4 with time with (speed rate) with full

L4: (283) buffer\$4 with time with (speed rate) with full

L5: (571) shln.xp

L6: (785925) buffer

L7: (348) 5 and 6

L8: (575) shln.xa

L9: (57) 6 and 8 and early

L10: (234) 6 with full with empty with time

L11: (705) 6 near? transfer\$4 near? free

L12: (35) (start\$4 begin\$5) with 11

L13: (0) calculate\$5 near? (time space) near? buffer

L14: (2289) calculate\$5 near? (time space) near? buffer

L15: (110) (remaining available) with 14

L16: (1) ('5944802').PN

L17: (0) FULL AND 16

L18: (1) EMPTY AND 16

 Failed Saved Favorites Logged (8) IDC

Dp: US PGPUB USPAT USOCR EPO JPO DEPATENT IBM TDS

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	U	Document ID	Issue Date	Pages	Title	Current CR	Current Xref	Retrieval Classif	U
1	<input checked="" type="checkbox"/>	US 20040064607 A1	20040401	14	Electronic apparatus and startup control	710/37		OdaKura, Ya	
2	<input checked="" type="checkbox"/>	US 5944802 A	19990831	11	Dynamic speed matching of host channel	710/32	710/53;	Seijo; Keith /	
3	<input checked="" type="checkbox"/>	US 6516359 B1	20030224	17	Information processing method and a	710/52	709/219;	Kurihara; Na	
4	<input checked="" type="checkbox"/>	US 5450546 A	19950912	43	Intelligent hardware for automatically	710/57	710/1	Krakhtan; Ci	
5	<input checked="" type="checkbox"/>	US 5884099 A	19990316	18	Control circuit for a buffer memory to	710/52	710/57;	Klingelhofer;	
6	<input checked="" type="checkbox"/>	US 5729708 A	19980317	82	Portable data buffer apparatus with m	710/52		Satoh; Mak	
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